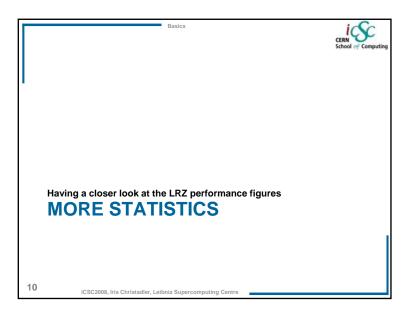
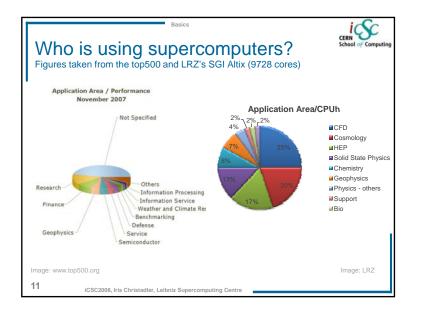
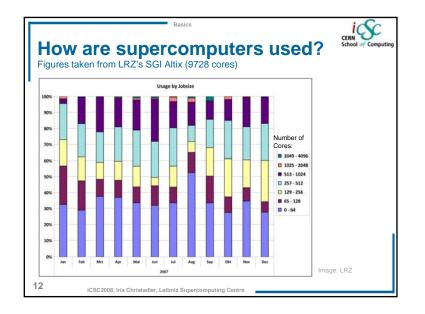


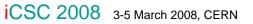
The actual top5									
	No	Site	System	#Proc	Peak Perf	Linpack	Efficency		
	1	LLNL, USA	IBM Blue Gene/L	212992	596378	478200	80%		
	2	FZJ, Germany	IBM Blue Gene/P	65536	222822	167300	75%		
	3	NMCAC, USA	SGI Altix ICE 8200	14336	172032	126900	74%		
	4	India	Cluster Platform, HP	14240	170880	117900	69%		
	5	Sweden	Cluster Platform, HP	13728	146430	102800	70%		
	15	LRZ, Germany	SGI Altix 4700	9728	62259	56520	91%		
	16	Japan	Sun + ClearSpeed	11664	102021	56430	55%		
	17	EPCC, UK	HECToR, Cray XT4	11328	63436	54648	86%		
9	9 ICSC2008, Iris Christadler, Leibniz Supercomputing Centre								

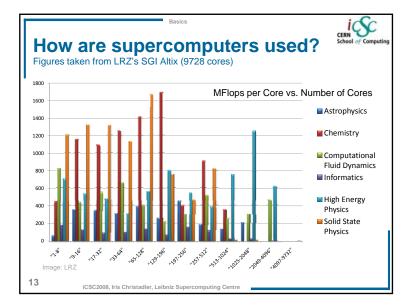


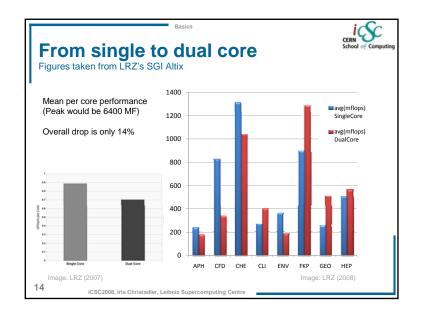


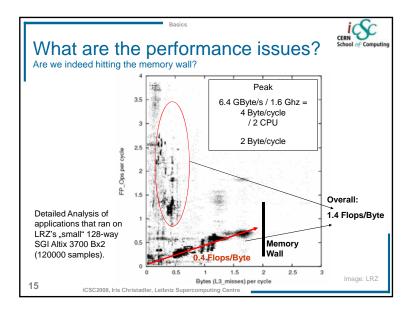


Towards Reconfigurable HPC Basics Lecture 1



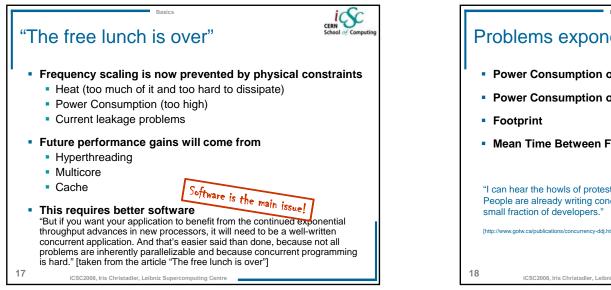


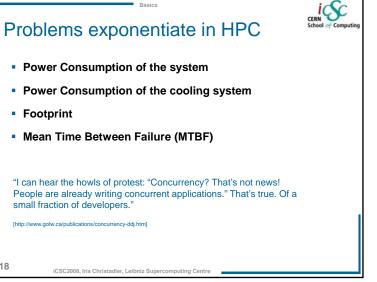


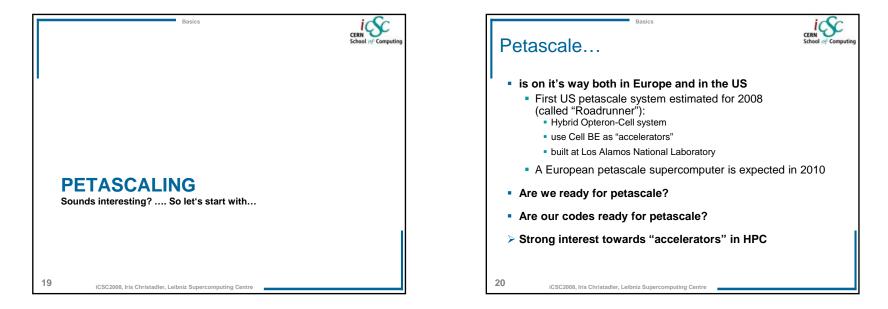


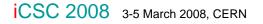




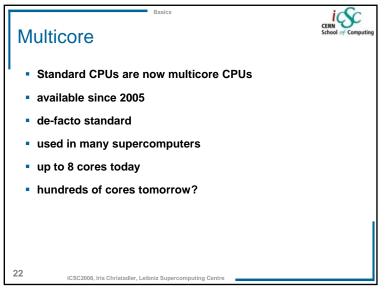


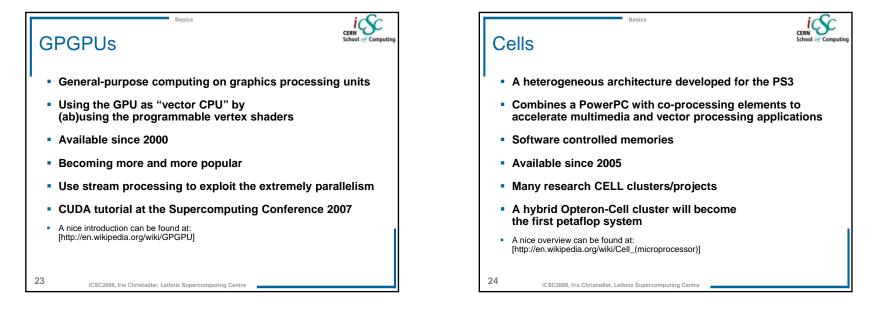












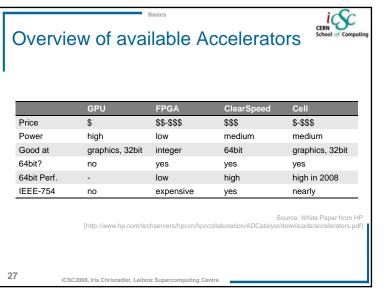


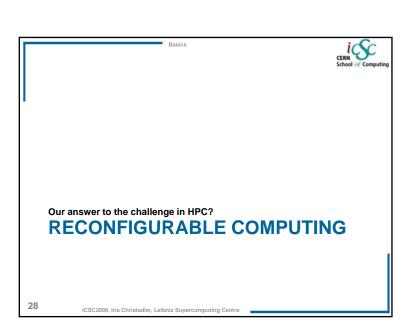


- Field-programmable gate array
- Adjust the architecture to the needs of your algorithm"
- Invented 1984
- Used heavily in embedded and real-time systems
- Used in supercomputers like Cray XD1, SGI RASC Blades
- Programmability!
- An overview can be found at: [http://en.wikipedia.org/wiki/Field-programmable_gate_array]

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ClearSpeed Boards

Advertisement claims:

Linpack results:

26

 Accelerator boards specially developed for scientific computing and the needs of the HPC community

"World's highest performance processor" (80.64 GF per board)
"World's highest performance per watt" (2 GF/Watt)

4 nodes (16GB) w/o Advance boards

1 node (16GB) w/o Advance boards

1 node (16GB) w/ 2 x Advance boards

4 nodes (16GB) w/ 2 x Advance boards each

Linpack Result

136.0 GF

364.2 GF

34.0 GF

90.1 GF

Only accelerated platform in the current top500

System Specification

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